P-Channel 30-V (D-S) MOSFET

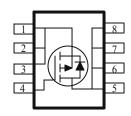
These miniature surface mount MOSFETs utilize High Cell Density process. Low r_{DS(on)} assures minimal power loss and conserves energy, making this device ideal for use in power management circuitry. Typical applications are PWMDC-DC converters, power management in portable and battery-powered products such as computers, printers, battery charger, telecommunication power system, and telephones power system.

•	Low r _{DS(on)} Provides Higher Efficiency and
	Extends Battery Life

- Miniature SO-8 Surface Mount Package Saves Board Space
- High power and current handling capability
- Extended VGS range (±25) for battery pack applications

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)} m(\Omega)$	I _D (A)		
-30	$9 @ V_{GS} = -10V$	-15		
-30	$13 @ V_{GS} = -4.5V$	-11		





ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)					
Parameter	Symbol	Maximum	Units		
Drain-Source Voltage			-30	V	
Gate-Source Voltage		V_{GS}	±25	V	
C . D . C . a	$T_A=25^{\circ}C$]] _{T_}	-15		
Continuous Drain Current ^a	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	1D	-11	A	
Pulsed Drain Current ^b	I_{DM}	±50			
Continuous Source Current (Diode Conduction) ^a		I_S	-2.1	A	
D	$T_A=25^{\circ}C$	D	3.1	W	
Power Dissipation ^a	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	LD	2.3	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum	Units	
Maximum Junction-to-Case ^a	t <= 5 sec	$R_{ heta JC}$	25	°C/W	
Maximum Junction-to-Ambient ^a	t <= 5 sec	$R_{\theta JA}$	50	°C/W	

1

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Developed	Cl1	T A C 122	Limits			TI24	
Parameter	Symbol Test Conditions		Min	Тур	Max	Unit	
Static							
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250 \text{ uA}$	-1			V	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}$			-1	uA	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			-5		
On-State Drain Current ^A	$I_{D(on)}$	$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	-50			Α	
	, ,	$V_{GS} = -10 \text{ V}, I_D = -13 \text{ A}$			9		
Drain-Source On-Resistance ^A	r _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -11 \text{ A}$			13	mΩ	
		$V_{GS} = -10 \text{ V}, I_D = -13 \text{ A}, TJ = 55^{\circ}\text{C}$			11		
Forward Tranconductance ^A	g_{fs}	$V_{DS} = -5 \text{ V}, I_{D} = -13 \text{ A}$		44		S	
Diode Forward Voltage	V_{SD}	$I_S = 2.1 \text{ A}, V_{GS} = 0 \text{ V}$		-0.7		V	
Dynamic ^b							
Total Gate Charge	Q_{g}	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V},$ $I_{D} = -13 \text{ A}$		37.0		nC	
Gate-Source Charge	Q_{gs}			10.0			
Gate-Drain Charge	Q_{gd}	I _D = -13 A		14.5			
Switching							
Turn-On Delay Time	$t_{d(on)}$			19			
Rise Time t ₁		$V_{DD} = -15 \text{ V}, R_L = 6 \Omega, ID = -1 \text{ A},$		11		nS	
Turn-Off Delay Time	$t_{d(off)}$	VGEN = -10 V		121			
Fall-Time	t_{f}			68			

Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

Analog Power (APL) reserves the right to make changes without further notice to any products herein. APL makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APL assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APL data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APL does not convey any license under its patent rights nor the rights of others. APL products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the APL product could create a situation where personal injury or death may occur. Should Buyer purchase or use APL products for any such unintended or unauthorized application, Buyer shall indemnify and hold APL and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that APL was negligent regarding the design or manufacture of the part. APL is an Equal Opportunity/Affirmative Action Employer.

PRELIMINARY

Typical Electrical Characteristics (P-Channel)

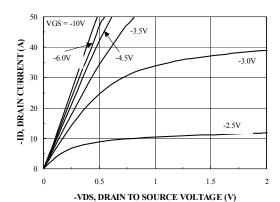


Figure 1. On-Region Characteristics

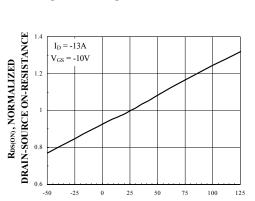


Figure 3. On-Resistance Variation with Temperature

T_J, JUNCTION TEMPERATURE (°C)

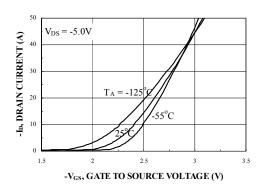


Figure 5. Transfer Characteristics

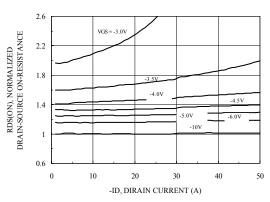


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage

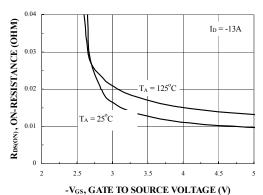
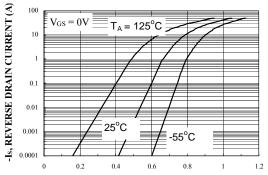


Figure 4. On-Resistance Variation with

Gate to Source Voltage

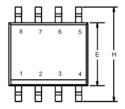


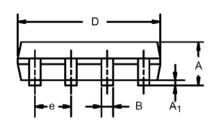
 $\hbox{-}V_{SD},\ BODY\ DIODE\ FORWARD\ VOLTAGE\ (V)$ Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature

Typical Electrical Characteristics (P-Channel) 6000 $I_D = -13A$ -V_{GS}, GATE-SOURCE VOLTAGE (V) f = 1 MHz $V_{\rm DS} = -10V$ CAPACITANCE (pF 4000 3000 -20V 2000 1000 Qg, GATE CHARGE (nC) - V_{DS} , DRAIN TO SOURCE VOLTAGE (V) Figure 7. Gate Charge Characteristics Figure 8. Capacitance Characteristics R_{DS(ON)} LIMIT -Ib, DRAIN CURRENT (A) Peak Transient Power (W) SINGLE PULSE $= 125^{\circ} \text{C/W}$ 0.01 0.001 0.1 TIME (S) -V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 10. Single Pulse Maximum Power Dissipation Figure 9. Maximum Safe Operating Area NORMALIZED THERMAL TRANSIENT JUNCTION TO AMBIENT ROJA = 125°C/W 0.01 100 1000 PULSE TIME (S) Figure 11. Transient Thermal Response Curve

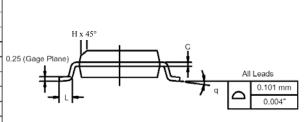
Package Information

SO-8: 8LEAD





	MILLIMETERS		INC	HES
Dim	Min	Max	Min	Max
Α	1.35	1.75	0.053	0.069
A ₁	0.10	0.20	0.004	0.008
В	0.35	0.51	0.014	0.020
С	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
е	1.27	BSC	0.050	BSC
Н	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°



5